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\*\* CONTINUING DATA \*\*\*\*\*

This application is a DIV of 10/153,525 05/24/2002 PAT 6,735,129

\*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN 2001-158449 05/28/2001

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

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Foreign Priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 12	TOTAL CLAIMS 5	INDEPENDENT CLAIMS 1
35 USC 119 (a-d) conditions <input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after met Allowance				

Verified and Acknowledged

Examiner's Signature \_\_\_\_\_ Initials \_\_\_\_\_

ADDRESS

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TITLE

SEMICONDUCTOR INTEGRATED CIRCUIT HAVING PROGRAMMABLE DELAYS FOR GENERATING TIMING SIGNALS WITH TIME DIFFERENCE BEING NON-INTEGRAL MULTIPLE OF CLOCK CYCLE

FILING FEE RECEIVED 770	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees ( Filing ) <input type="checkbox"/> 1.17 Fees ( Processing Ext. of time ) <input type="checkbox"/> 1.18 Fees ( Issue ) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit
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